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## Aerosol jet printed temperature sensors using an environmentally friendly bilayer dielectric

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# Aerosol jet printed temperature sensors using an environmentally friendly bilayer dielectric

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Supplementary material for this article is available [online](#)

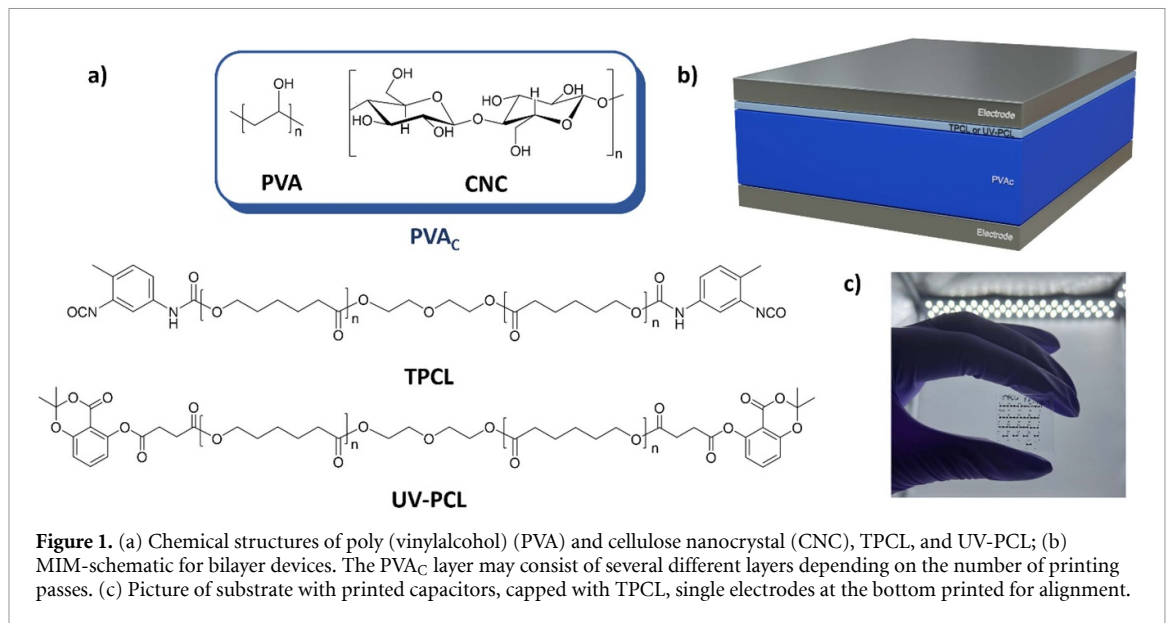
## Abstract

With the rise of the internet of things and applications such as smart packaging, the need for low cost, disposable temperature sensors with minimum environmental impact are critical. In this study, we report fully printed capacitive temperature sensors made from bio-degradable dielectric materials. All layers were aerosol jet printed and the areal capacitance was characterized at several temperatures between room temperature (22 °C) and 80 °C. Using a bilayer dielectric structure, a layer of poly (vinyl alcohol) (PVA) was encapsulated with polycaprolactone (PCL) through interfacial crosslinking to protect it against humidity. Various concentrations and layer amounts of PVA were investigated, with the most effective capacitors consisting of a single layer of PVA deposited from a 5.0 mg ml<sup>-1</sup> solution followed by a layer of the UV-crosslink-able PCL deposited from 2.0 mg ml<sup>-1</sup> solution, achieving a 43 ± 6% increase in areal capacitance at 80 °C when compared to room temperature, measured at a frequency of 501 Hz.

## 1. Introduction

The wearables and printed electronics industry has been growing steadily from its value of less than 1 billion US dollars in 2009, with expectations to be worth 73.3 billion dollars by 2029 [1, 2]. Printed electronics enables low-cost mass production of electronics that are being implemented in a variety of short term sensing applications, e.g. onto disposable packaging [3]. These sensors can be used to monitor a product's temperature, humidity, or other conditions indicative in supply chain management [3, 4]. The growth of the printed electronics industry also causes an increase in e-waste, with an estimated 74.7 million metric tons of e-waste produced worldwide by 2030 [5]. To reduce the impact of e-waste, it is critical to select materials which have a reduced environmental impact at the devices' end-of-life. Given this, the chosen manufacturing and printing processes of new electronics is directly influenced by the properties of the

new materials and corresponding ink properties, and therefore a simple substitution of a new material often leads to significant changes in the printing parameters. Although there are many options for sustainable dielectrics [6], there are few examples of printed electronics sensors fabricated using sustainable materials [7, 8]. Efforts have been made to demonstrate the printability of such sustainable materials with several printing methods allowing for direct writing for quick and simple customization [6, 9–15]. These printing techniques deposit the ink in a controlled area on the substrate which can reduce waste and enable printing on complex 3D surfaces [16–18]. As a result, the choice of process will also depend on the throughput, device area and complexity of the print, which may require techniques such as inkjet and aerosol jet over blade coating and roll to roll printing [9–11]. Inkjet and aerosol jet are non-contact techniques, providing better freedom of movement and adaption to irregular or 3D surfaces. In order to successfully



**Figure 1.** (a) Chemical structures of poly(vinylalcohol) (PVA) and cellulose nanocrystal (CNC), TPCL, and UV-PCL; (b) MIM-schematic for bilayer devices. The PVA<sub>C</sub> layer may consist of several different layers depending on the number of printing passes. (c) Picture of substrate with printed capacitors, capped with TPCL, single electrodes at the bottom printed for alignment.

print on such surfaces or with complex materials, a curing step can be incorporated, which is often termed 4D-printing [15, 19]. Inkjet printing typically requires low viscosity inks whereas aerosol jet printing is amenable to a larger range of viscosities [11]. Furthermore, while inkjet printing is often successful, aerosol jet has a larger working distance, ensuring more distance to the target, and the atomization of the functional inks that can inherently suppress surface tension effects which may lead to poor patterning [13, 14, 20].

One way to fill this gap within the smart packaging industry is to improve the mechanical and electrical properties of commercially available sustainable materials [21]. We recently reported improved dielectric materials with negligible moisture sensitivity using bilayers of poly(vinyl alcohol) (PVA) and polycaprolactone (PCL) [22, 23]. Both materials are compostable and currently produced on the ton scale, making them good candidates to be quickly adopted into printed electronics manufacturing [24, 25]. The PCL polymer was functionalized with either toluene diisocyanate (TPCL) end groups or benzodioxinone (UV-PCL) end groups, enabling thermal or UV-crosslinking with the hydroxy groups of PVA at the PVA/PCL interface. PVA has been reported to exhibit changes in electrical properties when exposed to changes in temperature [26]. However, PVA is well known to be hygroscopic and has previously been applied as a humidity sensor [27–30]. Including a thin PCL coating (figure 1) with interfacial crosslinking creates a barrier to moisture improving the stability of PVA and allows for further orthogonal processing, which is essential to decouple the temperature and humidity response [23, 31]. For example, when fabricating thin-film transistors (TFTs) with a PVA dielectric, an increase in temperature will increase device hysteresis which is due to a change

in the frequency dependant capacitance of PVA [26, 32, 33]. This is caused by the PVA softening with an increase in temperature which facilitates the alignment of space charges present due to imperfections and the hydroxy groups with the applied electric field increasing the capacitance [26, 34, 35].

In this work, a PVA/cellulose nanocrystal blend (PVA<sub>C</sub>) was used as the high-*k* dielectric material ( $k \sim 12$ ) [23] while either TPCL or UV-PCL as a low-*k* ( $k \sim 4$ ) [23, 31] dielectric material to create high-*k*/low-*k* bilayer dielectrics. Metal-insulator-metal (MIM) capacitors were fabricated on glass substrates using aerosol jet printing. A silver nanoparticle ink was used to print the top and bottom electrodes. These devices were characterized at various temperatures to analyze their change in capacitance vs temperature. Three different MIM capacitor configurations were investigated: PVA<sub>C</sub>, PVA<sub>C</sub>/TPCL and PVA<sub>C</sub>/UV-PCL and in all cases an increase in capacitance was measured in response to an increase in temperature from 22 °C to 80 °C. These findings demonstrate that fully printed temperature sensors can be fabricated using a low cost environmentally friendly bilayer dielectric system.

## 2. Experimental

### 2.1. Materials

The silver electrodes were printed using a Novacent Metalon JS-A221AE silver nanoparticle ink with 50 wt% loading. PVA  $M_w = 31\,000\text{--}50\,000$  Da; 98%–99% hydrolyzed, PCL diol ( $M_w = 2000$  Da) and toluene-2,4-diisocyanate (95%) and were purchased through Sigma-Aldrich. TPCL and UV-PCL were synthesized following previously reported procedures [31]. The PVA was dissolved in distilled water with 0.75 wt% CNCs dispersed in the solutions at PVA concentrations of 5.0 and 10.0 mg ml<sup>-1</sup>. The TPCL

and UV-PCL were dissolved in anhydrous toluene at concentrations of 2.0 mg ml<sup>-1</sup>.

## 2.2. Printing

An Optomec Aerosol Jet FLEX (AJ5X) printer was used to fabricate the MIM capacitors. The PVA<sub>C</sub> was printed at concentrations of 5 and 10 mg ml<sup>-1</sup> with the following settings. Nozzle size: 300 μm, plate temperature: 25 °C, sheath: 100 ccm, atomizer power: 45 V, atomizer: 40 ccm and speeds of 2 mm s<sup>-1</sup>. TPCL and UV-CPL were printed at 2.0 mg ml<sup>-1</sup> with the following settings: nozzle: 300 μm, plate temperature: 25 °C, sheath: 150 ccm, atomizer power: 23 V, atomizer: 40 ccm and speeds of 2 mm s<sup>-1</sup>. The silver nanoparticle ink was printed with the following settings. Sheath: 100 ccm, atomizer power: 45 V, atomizer: 45 ccm and speeds of 2 mm s<sup>-1</sup>. The top silver electrode was printed over twice to avoid having any breaks between the top electrode and the contact pads due to the height of the dielectric layer.

## 2.3. Optical profilometry

2D and 3D profilometry scans were completed using a cyberTECHNOLOGIES CT100 optical profilometer (cyberTECHNOLOGIES GmbH) and Golden Surfer® 11 Software to visualize the surface and side profiles for the 3D imaging.

## 2.4. Electrical characterization

The capacitors were tested using a PGSTAT204 potentiostat from Metrohm Autolab connected with two electrical probes to contact the capacitor electrodes with, placed inside a Faraday cage. Using the Nova 2.1.3 software from Metrohm Autolab for control, the real as well as imaginary parts of the impedance  $Z$  together with their phase angle  $\delta$  were measured at 10 different frequencies per decade (logarithmically spaced out), between 1 MHz and 0.1 Hz. However, below 100 Hz the measurements became exceedingly noisy, leaving an effective range of 1 MHz to 100 Hz. In potentiostatic measurements, the frequency dependant capacitance is calculated from the real and imaginary portions of the impedance,  $Z'$  and  $Z''$  respectively, using the following equation [36]

$$C = \frac{1}{2\pi f} \left( \frac{Z''}{Z''^2 + Z'^2} \right). \quad (1)$$

After letting the substrates reach the specific temperature on an RCT basic hotplate from IKA for at least 10 min, the testing was repeated for each working device at 40 °C, 50 °C, 60 °C, 70 °C, and 80 °C.

## 3. Results and discussion

### 3.1. Microscope imaging

The capacitance scales inversely with material thickness, where the total capacitance of multilayered materials is characterized by equation (2). Therefore,

the lowest capacitance will have a large impact on the total capacitance of the dielectric system [37, 38],

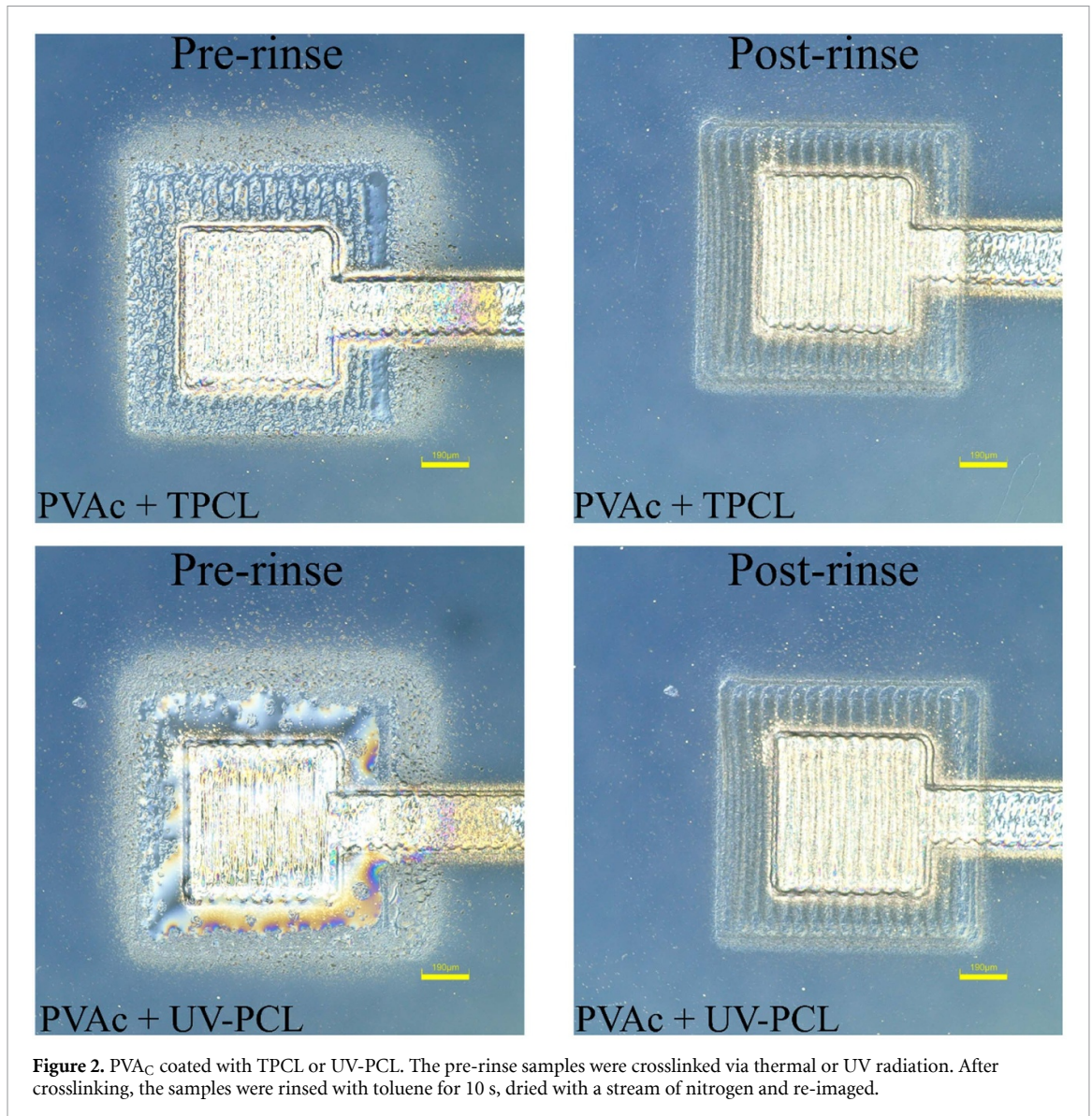
$$\frac{1}{C_{\text{tot}}} = \frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3} + \dots \quad (2)$$

It is challenging to print uniform thin layers (<100 nm) by aerosol jet printing and as such, it is difficult to fabricate bilayer dielectrics without the appearance of defects, such as pinholes. However, in our previous paper, we showed it to be possible to deposit a thick layer of TPCL or UV-PCL, which when crosslinked to the PVA interface and subsequently rinsed with toluene to remove any excess PCL, led to a very thin layer of TPCL or UV-PCL on the PVA<sub>C</sub> [23, 31]. This strategy enables the fabrication of a pin-hole free thin low- $k$  layer by forming a monolayer and preventing pooling and delamination. To demonstrate this, figure 2 shows microscope images of aerosol jet printed PVA<sub>C</sub> with a TPCL or UV-PCL top layer, before and after toluene rinsing for 10 s. The TPCL was thermally crosslinked, and the UV-PCL was crosslinked under UV light prior to imaging. Figure 2 shows that the excess PCL can be removed with a simple rinsing step leaving behind a thin layer of PCL on top of the PVA<sub>C</sub>.

### 3.2. Layer thickness comparison

The primary goal of this study was to demonstrate a fully printed PVA/PCL bilayer capacitor system, and as such various printing techniques were considered and investigated. Initial depositions demonstrated that blade coating PVA from a water-based solution caused the films to dry unevenly on the silver electrodes due to poor surface adhesion as well as the low volatility of water. We explored a variety of solvent additives, blade geometries and processing conditions with little reproducible success. Furthermore, the dewetting challenges could not be rectified by employing other deposition techniques such as spray-coating or slot-die coating (see figure S1). Alternatively, aerosol jet printing showed promise to circumvent the PVA film forming challenges. Aerosol jet printing atomizes the polymer solution enabling it to quickly dry on the substrate and reduce the coffee ring effect seen with other techniques that have slower drying times [12]. The use of aerosol jet printing has been previously demonstrated for depositing dielectric materials for both capacitor and TFTs [11, 39–44]. Several of these studies have highlighted the usefulness of aerosol jet printing for PVA specifically, as the dielectric material [39, 41, 42]. Therefore, we employed aerosol jet printing for the fabrication of our fully printed bilayer dielectric based temperature sensors.

As detailed in the Experimental section, the silver electrode was first printed, followed by the printing of PVA<sub>C</sub>, then either TPCL or UV-PCL followed by another silver electrode. After the deposition of the TPCL or UV-PCL, interfacial crosslinking was

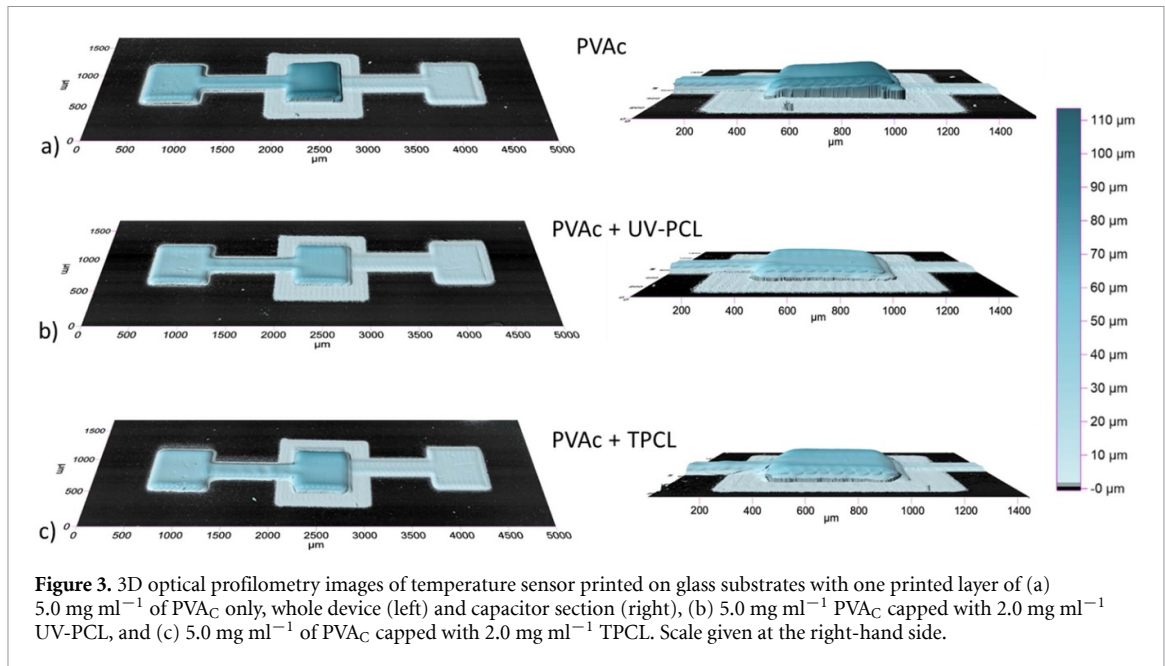


**Figure 2.** PVA<sub>C</sub> coated with TPCL or UV-PCL. The pre-rinse samples were crosslinked via thermal or UV radiation. After crosslinking, the samples were rinsed with toluene for 10 s, dried with a stream of nitrogen and re-imaged.

activated by either heat or UV-light prior to the final electrode deposition [23, 31]. Figure 3 highlights 3D optical profilometry scans of the sensors fabricated with the PVA<sub>C</sub> with UV-PCL or TPCL ( $2.0 \text{ mg ml}^{-1}$ ) bilayer compared to bare PVA<sub>C</sub> as a baseline to analyze the effectiveness of the PCL layer on preventing moisture contamination. After having been characterized and stored in ambient air for five days, it is evident that when only using PVA<sub>C</sub> with no PCL, the overall height of the device is greater ( $120 \text{ }\mu\text{m}$ ) than when coating the PVA<sub>C</sub> with TPCL ( $82 \text{ }\mu\text{m}$ ) or with UV-PCL ( $78 \text{ }\mu\text{m}$ ). This observation is reproducible and is consistent with PVA swelling with the absorption of moisture [31], demonstrating that the PCL provides a successful moisture barrier for the PVA.

In attempts to optimize device fabrication and the resulting performance, we explored different solution concentrations as well as depositing multiple

PVA<sub>C</sub> layers to increase the overall dielectric thickness. Tables 1 and S1 summarize the film optimization through characterization of average height, maximum height, average roughness, and root-mean-square (RMS) roughness of the polymer layer. Measurements were taken along the polymer edge adjacent to the center of the device, as shown in figure S2. When using a  $5.0 \text{ mg ml}^{-1}$  of PVA<sub>C</sub>, an additional layer (2 deposition of the same solution) was measured to have roughly doubled the height for both uncapped devices and those capped with UV-PCL (table 1). A slight increase in the roughness of the corresponding films made from  $5.0 \text{ mg ml}^{-1}$  was also measured with the addition of a second layer. The devices capped with TPCL measured a smaller increase in height of approximately  $2 \text{ }\mu\text{m}$  with the additional layer of PVA<sub>C</sub>, compared to the other devices, whose overall thickness



**Table 1.** Summary of 2D optical profilometry scans for devices containing increasing layers of  $5.0 \text{ mg ml}^{-1}$  PVAc and those capped with  $2.0 \text{ mg ml}^{-1}$  of UV-PCL and TPCL; including the maximum change in areal capacitance.

Exp. conditions <sup>a</sup> with # layers	$H_{AVE}$ <sup>b</sup> (mm)	$H_{MAX}$ <sup>b</sup> (mm)	$R_{AVE}$ <sup>b</sup> (mm)	$R_{RMS}$ <sup>b</sup> (mm)	$\Delta C_0$ <sup>c</sup> (%)	
1 × PVAc	$2.95 \pm 0.39$	$4.60 \pm 0.54$	$0.36 \pm 0.06$	$0.46 \pm 0.08$	$36 \pm 2$	
2 × PVAc	$6.35 \pm 0.19$	$8.26 \pm 0.26$	$0.41 \pm 0.07$	$0.51 \pm 0.07$	$44 \pm 7$	
1 × PVAc 2 × PVAc	1 × UV-PCL	$2.90 \pm 0.12$ $6.09 \pm 0.63$	$4.78 \pm 0.54$ $8.26 \pm 0.46$	$0.48 \pm 0.06$ $0.56 \pm 0.07$	$0.56 \pm 0.07$ $0.68 \pm 0.09$	$43 \pm 6$ $38 \pm 3$
1 × PVAc 2 × PVAc	1 × TPCL	$3.02 \pm 0.31$ $4.99 \pm 0.79$	$4.49 \pm 0.32$ $7.15 \pm 1.01$	$0.28 \pm 0.03$ $0.44 \pm 0.12$	$0.36 \pm 0.04$ $0.54 \pm 0.13$	$36 \pm 2$ $44 \pm 7$

<sup>a</sup> PVAc layer was deposited using  $5.0 \text{ mg ml}^{-1}$  solution in distilled water. UV-PCL and TPCL layers were deposited using  $2.0 \text{ mg ml}^{-1}$  solution in anhydrous toluene. # of Layers refers to the deposition passes of the PVAc solution prior to UV-PCL or TPCL.

<sup>b</sup>  $H_{AVE}$  and  $H_{MAX}$  are the average and max height (thickness) of the film and  $R_{AVE}$  and  $R_{RMS}$  are the average roughness and RMS roughness respectively.

<sup>c</sup>  $\Delta C_0$  is the relative percent (%) change in areal capacitance between room temperature and  $80^\circ \text{C}$  taken at 501 Hz.

increased by  $3 \mu\text{m}$ . Upon increasing the PVAc concentration to  $10.0 \text{ mg ml}^{-1}$ , single-layer films exhibited a similar initial thickness, and slightly thicker secondary layers of  $3.80$  and  $4.04 \mu\text{m}$  for the UV-PCL and TPCL layers respectively (table S1). The first layer of devices capped with UV-PCL was notably thinner, leading to overall thinner stack which could account for the variation in measured average roughness and RMS roughness compared to bare PVAc and PVAc/TPCL. Overall, these results demonstrate that PVAc/UV-PCL and PVAc/TPCL bilayers can be successfully printed using aerosol jet printing, where roughness and thickness can be easily controlled through sequential depositions, and choice of solution concentration does not affect the print quality.

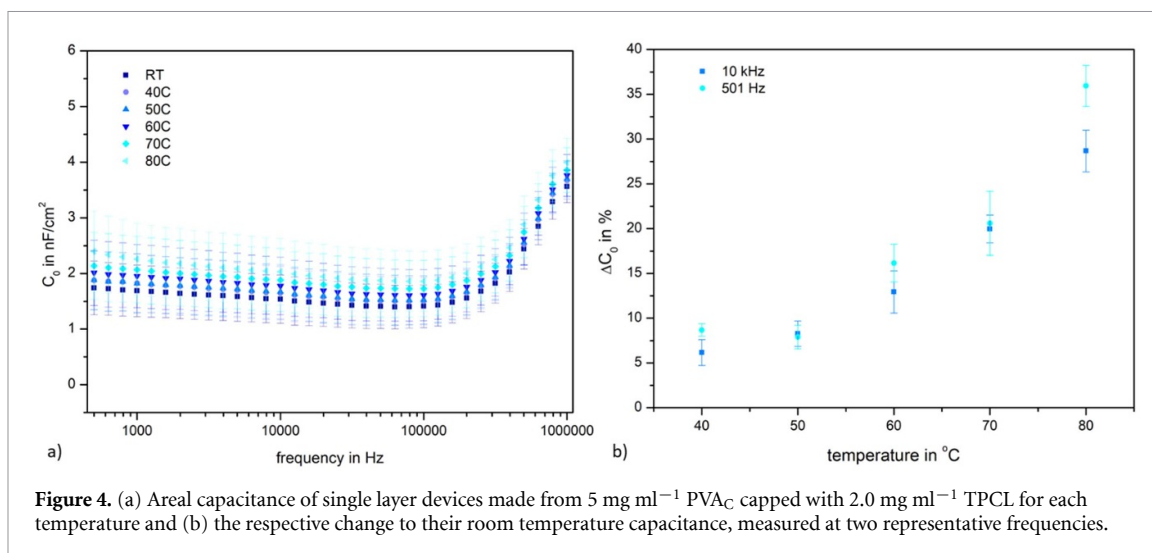
### 3.3. Capacitor

The capacitors were characterized by their areal capacitance  $C_0$  rather than their relative permittivity  $k$ .

The best performing devices were obtained using a single layer of  $5.0 \text{ mg ml}^{-1}$  PVAc capped with  $2.0 \text{ mg ml}^{-1}$  TPCL, and their electrical characterization is shown in figure 4. All other devices and conditions are found in the supporting information (tables S2 and S3). Figure 4(a) demonstrates the consistent change in  $C_0$  as a function of operating frequency and temperature. A minimum of four devices were printed for every condition, and with a total measured area of  $0.39 \pm 0.02 \text{ mm}^2$ , averaged together led to significant error bars. The relative change of the  $C_0$  of each device was calculated using the following equation:

$$\Delta C_0 = \left( \frac{C_{0T} - C_{0RT}}{C_{0RT}} \right) * 100\% \quad (3)$$

with  $C_{0RT}$  being the areal capacitance at room temperature, and  $C_{0T}$  the areal capacitance at the specific temperature. We note that we consistently observed



**Figure 4.** (a) Areal capacitance of single layer devices made from  $5 \text{ mg ml}^{-1}$  PVA<sub>C</sub> capped with  $2.0 \text{ mg ml}^{-1}$  TPCL for each temperature and (b) the respective change to their room temperature capacitance, measured at two representative frequencies.

an increase in  $C_0$  for all devices above 1 MHz, regardless of the polymer thickness or amount of layers and therefore it is a result of the device architecture, electrode selection and/or operating conditions. We believe this increase is due to the transition between capacitive region to inductive region where parasitic inductance becomes more significant and we begin to observe electrode skin effects, proximity effects and dielectric loss effects, which can all increase the capacitance at high frequency. These results further demonstrate the importance of operating conditions, such as frequency, when characterizing capacitors. As can be seen in figure 4(b), the  $\Delta C_0$  with temperature was statistically significant for each device, with a greater increase in  $\Delta C_0$  at lower frequencies upon temperature increase compared to higher frequencies. At the lowest reliable frequency of 501 Hz, which consistently gave percentual changes in capacitance larger than the measured noise,  $\Delta C_0$  increased by  $36\% \pm 2\%$  at  $80^\circ\text{C}$  compared to room temperature, while it was only an increase of  $11\% \pm 3\%$  at the highest reliable frequency of 1.0 MHz (figure S4).

Further characterization of devices fabricated using uncapped  $5.0 \text{ mg ml}^{-1}$  PVA<sub>C</sub> is summarized in figure S4, while the remaining characterization of devices capped with TPCL and UV-PCL are found in figures S5 and S6, respectively. A summary of calculated  $\Delta C_0$  values at  $40^\circ\text{C}$  and  $80^\circ\text{C}$  can be found in table S2. Devices made from one and two layers of  $5.0 \text{ mg ml}^{-1}$  PVA<sub>C</sub>, uncapped and capped with TPCL showed similar electrical responses to those capped with UV-PCL, however their response had greater variability (error bars) at lower temperatures. Furthermore, we found that for devices printed from  $5.0 \text{ mg ml}^{-1}$  PVA<sub>C</sub> an increase in height also slightly increased the performance in uncapped capacitors and those capped with TPCL, as summarized in table 1. However, devices with  $5.0 \text{ mg ml}^{-1}$  PVA<sub>C</sub> capped with UV-PCL output a higher signal for thinner devices, with bilayers having a  $\Delta C_0 = 38 \pm 3\%$  at

$80^\circ\text{C}$  and 501 Hz, while single layer devices showed a  $\Delta C_0 = 43 \pm 6\%$ . At higher frequencies, the areal capacitance for the single layer device was lower with only  $3.6 \pm 0.7\%$  when measured at 1.0 MHz.

Optimal devices were obtained using a single or double layer of PVA<sub>C</sub> capped with TPCL or UV-PCL. An improvement in signal was often reported when going from a single to a double layer of PVA<sub>C</sub> as the film thickness increased. When increasing to a third layer we observed a significant increase in the error associated to the uneven film formation and thickness variation, leading to unreliable devices, as demonstrated to figures S6 and S7.

## 4. Conclusion

We present fully printed capacitive temperature sensors that contain sustainably driven biodegradable polymers as dielectric materials. It was shown that single layer and multi-layer devices at varying concentrations could be successfully fabricated with aerosol jet printing, resulting in uniform layers with no excessive change in surface roughness of the devices. The percentual change of  $\Delta C_0$  increased consistently with temperature, with the low variability were measured at frequencies around 500 Hz to ensure the strongest  $\Delta C_0$  response. For single PVA<sub>C</sub> layer devices printed from  $5.0 \text{ mg ml}^{-1}$  PVA<sub>C</sub>, capping with UV-PCL not only provided protection against ambient humidity for the PVA<sub>C</sub> but also gave the strongest  $\Delta C_0$  of  $43 \pm 6\%$  at  $80^\circ\text{C}$ . For devices with two layers of PVA<sub>C</sub>, capping with TPCL yielded the best capacitors with  $\Delta C_0$  of  $44 \pm 7\%$  at  $80^\circ\text{C}$ .

## Data availability statement

All data that support the findings of this study are included within the article (and any supplementary files).

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